

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:	
Prasenjit Biswas et al.	Examiner: Unknown
Serial No.: Unknown	Art Unit: Unknown
Filed: March 8, 2004	
For: FLOATING POINT UNIT PIPELINE SYNCHRONIZED WITH PROCESSOR PIPELINE	

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR §§1.97-1.98**

Sir:

In accordance with the duty of disclosure under 37 CFR §1.56 and pursuant to 37 CFR §§1.97-1.98, Applicants hereby notify the U.S. Patent and Trademark Office of the references listed on the attached Form PTO-1449. Pursuant to the PTO's waiver of the requirements under 37 CFR 1.98(a)(2)(i) for submitting a copy of each cited U.S. patent/patent application for new applications filed after June 30, 2003, we are not submitting copies of the U.S. patents cited. However, we are enclosing copies of all non-patent literature documents cited.

The submission of the listed documents is not intended as an admission that any such document constitutes prior art against the claims of the present application. Applicants reserve the right to dispute any of the listed documents as prior art during examination. Furthermore, Applicants do not waive any right to take any action that

would be appropriate to antedate or otherwise remove any listed document as a competent reference against the claims of the present application. The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made or that no other material information may exist.

The Examiner is requested to initial the enclosed Form PTO-1449 and return a copy thereof to the undersigned.

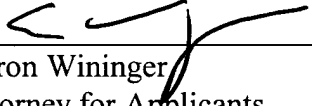
If for any reason an insufficient fee has been paid, please charge the insufficiency to Deposit Account No. 05-0150.

Dated: 3/8/04

Respectfully Submitted,
Prasenjit Biswas et al.

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

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of

2

Complete if Known

Application Number	Unknown
Filing Date	March 8, 2004
First Named Inventor	Prasenjit Biswas
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	43496.00014

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code ² (if known)			
		US- 3,242,465	03-22-1966	Gloates et al.	
		US- 4,171,537	10-16-1979	Weissberger et al.	
		US- 4,729,094	03-01-1988	Zolnowsky et al.	
		US- 4,979,102	12-18-1990	Tokuume, Takahiro	
		US- 5,029,073	07-02-1991	Takaya et al.	
		US- 5,053,631	10-01-1991	Perlman et al.	
		US- 5,129,065	07-07-1992	Priem et al.	
		US- 5,193,159	03-09-1993	Hashimoto et al.	
		US- 5,487,022	01-23-1996	Simpson et al.	
		US- 5,559,977	09-24-1996	Avnon et al.	
		US- 5,664,138	09-02-1997	Yoshida, Takeshi	
		US- 5,860,000	01-12-1999	Biswas et al.	
		US- 6,003,124	12-14-1999	Laborie, Jean-Louis	
		US- 6,012,139	01-04-2000	Biswas et al.	
		US-			
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FOREIGN PATENT DOCUMENTS

Examiner Initials *	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Application Number	Unknown
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First Named Inventor	Prasenjit Biswas
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	43496.00014

NON PATENT LITERATURE DOCUMENTS

Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		SMITH, James E. et al.; "Implementing Precise Interrupts in Pipelined Processors"; IEEE Transactions on Computers; May 1988; pages 562-573; Vol. 37, No. 5.	
		HASEGAWA, Atsushi et al.; "SH3: High Code Density, Low Power"; IEEE Micro; December 1995; pages 11-19.	
		TURLEY, Jim; "Hitachi Adds FP, DSP Units to SuperHC Chips - Two New Additions Aimed at Boosting Lead in Consumer Electronics"; Microprocessor Report; December 4, 1995; pages 10-11.	
		JOHNSON, Mike; "Superscalar Microprocessor Design"; Prentice Hall; 1991; 7 pages.	
		MARGULIS, Neal; "i860 Microprocessor Architecture"; Osborne McGraw-Hill; 1990; pages 27, 37-41, 45-53, 80-94 and 205-210.	
		iAPX 86/88, 186/188 User's Manual Hardware Reference; Intel; Order No. 210912-001; 3 pages, including page 3-8 (8087 Numeric Processor Extension).	

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